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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/925,002	08/08/2001	Tom-Chin Chang	JCLA 7428	3609

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EXAMINER

BAKER, CHARLOTTE M

ART UNIT	PAPER NUMBER
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2626

DATE MAILED: 12/23/2005

Please find below and/or attached an Office communication concerning this application or proceeding.

Office Action Summary	Application No. 09/925,002	Applicant(s) CHANG ET AL.	
	Examiner Charlotte M. Baker	Art Unit 2626	

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) ☒ Responsive to communication(s) filed on RCE filed on 12/01/2005.
- 2a) ☐ This action is **FINAL**. 2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) ☒ Claim(s) 1-5 and 7-16 is/are pending in the application.
- 4a) Of the above claim(s) _____ is/are withdrawn from consideration.
- 5) ☒ Claim(s) 1-5, 7 and 8 is/are allowed.
- 6) ☒ Claim(s) 9-16 is/are rejected.
- 7) ☐ Claim(s) _____ is/are objected to.
- 8) ☐ Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

- 9) ☒ The specification is objected to by the Examiner.
- 10) ☒ The drawing(s) filed on 08 August 2001 is/are: a) ☒ accepted or b) ☐ objected to by the Examiner.
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

- 12) ☒ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☐ All b) ☐ Some * c) ☒ None of:
1. ☐ Certified copies of the priority documents have been received.
2. ☐ Certified copies of the priority documents have been received in Application No. _____.
3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).
- * See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

- | | |
|--|---|
| 1) <input checked="" type="checkbox"/> Notice of References Cited (PTO-892) | 4) <input type="checkbox"/> Interview Summary (PTO-413)
Paper No(s)/Mail Date. _____ |
| 2) <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948) | 5) <input type="checkbox"/> Notice of Informal Patent Application (PTO-152) |
| 3) <input type="checkbox"/> Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)
Paper No(s)/Mail Date _____ | 6) <input type="checkbox"/> Other: _____ |

DETAILED ACTION

Priority

1. Acknowledgment is made of applicant's claim for foreign priority based on an application filed in Taiwan, R.O.C on 06/08/2001. It is noted, however, that applicant has not filed a certified copy of foreign application number 90113920 application as required by 35 U.S.C. 119(b).

Claim Objections

2. Claims 3, 10, 14, and 15 are objected to because of the following informalities: in claim 3, replace "an sequentially" with --and sequentially--; in claim 10, replace "to the an application" with --to an application--; in claim 14, replace "analog/digital" with --analogue/digital--; in claim 14, replace "analog format" with --analogue format--; in claim 15, replace "to the an application" with --to an application--. Appropriate correction is required.

Claim Rejections - 35 USC § 102

3. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(e) the invention was described in (1) an application for patent, published under section 122(b), by another filed in the United States before the invention by the applicant for patent or (2) a patent granted on an application for patent by another filed in the United States before the invention by the applicant for patent, except that an international application filed under the treaty defined in section 351(a) shall have the effects for purposes of this subsection of an application filed in the United States only if the international application designated the United States and was published under Article 21(2) of such treaty in the English language.

4. Claims 9-16 are rejected under 35 U.S.C. 102(e) as being anticipated by Aoki (6,559,981).

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Regarding claim 9: Aoki discloses receiving a value from an even-numbered pixel and an odd-numbered pixel of a scanning device (Fig. 3, image reading apparatus)(col. 4, ln. 55-67)(col. 6, ln. 4-9); producing an odd-even compensation value (Fig. 3 shading correction circuit 3 and col. 4, ln. 52-57) by use of the even-numbered pixel value and the odd-numbered pixel value (pixel data which has been shading corrected, col. 4, ln. 55-57); and using the odd-even compensation value (Fig. 3, odd-even correction circuit 4) to compensate an even-numbered pixel value and an odd-numbered pixel value obtained from a scanning operation (col. 4, ln. 49-67) (col. 6, ln. 4-9).

Regarding claim 10: Aoki satisfies all the elements of claim 9. Aoki further discloses performing a plurality of alternate scanning operations (col. 8, ln. 6-17 and 25-37) on a document (Fig. 2, document 16) to obtain values corresponding to a plurality of alternately scanned pixels (col. 8, ln. 25-37); and digitizing the alternately scanned pixel values into even data values and odd data values (Fig. 3, A/D converter 2 and col. 4, ln. 49-67); and providing the even data values and the odd data values to an application specific integrated circuit (Fig. 3, odd-even correction circuit 4) (Examiner is interpreting the odd-even correction circuit 4 to be an application specific integrated circuit (asic) because this circuit has a specific purpose as outlined in the Aoki reference and the detail is shown in Fig. 1.).

Regarding claim 11: Aoki satisfies all the elements of claim 9. Aoki further discloses adding a corresponding odd-even compensation value to the even-numbered pixel value and the odd-numbered pixel value (col. 4, ln. 55-67) (col. 6, ln. 21-63) (col. 8, ln. 6-17 and 25-37).

Regarding claim 12: Aoki discloses circuitry adapted to receive a value from an even-numbered pixel and an odd-numbered pixel of a scanning device (Fig. 3, image reading apparatus)(col. 4, ln. 55-67)(col. 6, ln. 4-9); circuitry adapted to produce an odd-even

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compensation value (Fig. 3 shading correction circuit 3 and col. 4, ln. 52-57) by use of the even-numbered pixel value and the odd-numbered pixel value (pixel data which has been shading corrected, col. 4, ln. 55-57); and circuitry adapted to compensate the even-numbered pixel value and the odd-numbered pixel value (Fig. 3, odd-even correction circuit 4) (col. 4, ln. 49-67) (col. 6, ln. 4-9).

Regarding claim 13: Aoki satisfies all the elements of claim 12. Aoki further discloses an image memory unit (Fig. 3, controller 19) adapted to hold a plurality of image data values (col. 4, ln. 40-48); and an input/output interface (Fig. 3, interface 25) adapted to access the image data values (col. 4, ln. 40-48).

Regarding claim 14: Aoki satisfies all the elements of claim 12. Aoki further discloses an alternative-sensing device (Fig. 3, color image pick-up system 1) adapted to perform a plurality of alternate scanned operations (col. 8, ln. 6-17 and 25-37) on a document (Fig. 2, document 16) and sequentially obtain a plurality of values from alternately scanned pixels (col. 8, ln. 25-37); and an analog/digital converter (Fig. 3, A/D converter 2 and col. 4, ln. 49-67) coupled to the alternative-sensing device (Fig. 3, color image pick-up system 1) adapted to digitize the alternately scanned pixel values in analog format into even data values and odd data values and transfer the even data values and the odd data values to said circuitry to receive a value (col. 4, ln. 49-54).

Regarding claim 15: Aoki satisfies all the elements of claim 12. Aoki further discloses circuitry (Fig. 3, color image pick-up system 1) adapted to perform a plurality of alternate scanning operations (col. 8, ln. 6-17 and 25-37) on a document (Fig. 2, document 16) to obtain values corresponding to a plurality of alternately scanned pixels (col. 8, ln. 25-37); and circuitry

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(Fig. 3, A/D converter 2) adapted to digitize the alternately scanned pixel values into even data values and odd data values (col. 4, ln. 49-67); and circuitry adapted to provide the even data values and the odd data values (Fig. 3, output of shading correction circuit 3) to an application specific integrated circuit (Fig. 3, odd-even correction circuit 4) (Examiner is interpreting the odd-even correction circuit 4 to be an application specific integrated circuit (asic) because this circuit has a specific purpose as outlined in the Aoki reference and the detail is shown in Fig. 1.).

Regarding claim 16: Aoki satisfies all the elements of claim 12. Aoki further discloses adding a corresponding odd-even compensation value to the even-numbered pixel value and the odd-numbered pixel value (col. 4, ln. 55-67) (col. 6, ln. 21-63) (col. 8, ln. 6-17 and 25-37).

Allowable Subject Matter

5. Claims 1-5 and 7-8 are allowed.

Conclusion

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Charlotte M. Baker whose telephone number is 571-272-7459. The examiner can normally be reached on Monday-Friday 8:30-5:00.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Kimberly A. Williams can be reached on 571-272-7471. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

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Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).


CMB


MARK WALLERSON
PRIMARY EXAMINER